

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant: David E. McKinley
 Assignee: Zilog, Inc.
 Title: "DYNAMIC POWER MANAGEMENT OF SOLID STATE MEMORIES"
 Serial No.: Unknown Filed: Herewith
 Examiner: G. Davis Group Art Unit: 2308
 Attorney Docket No.: M-2689-1C US

17/E
 MDS
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San Jose, California
 January 4, 1995

COMMISSIONER OF PATENTS AND TRADEMARKS
 Washington, D. C. 20231

PRELIMINARY AMENDMENT

Sir:

After the above-identified application is granted a serial number, please amend the application as set forth below.

IN THE CLAIMS

Sub F1

1. (Twice Amended) A dynamic power management device for supplying power to a solid state memory integrated circuit in an electronic system, said device comprising:

power control means for supplying a variable voltage to said memory integrated circuit [independently from the] without varying a voltage being supplied to [the rest] elements of said electronic system other than said memory integrated circuit; and

logic control means for generating address and control signals for said memory integrated circuit and for controlling said power control means to supply power to said memory integrated circuit at a level to maintain memory information in said memory integrated circuit during periods of no data access activity and supplying power at a level to [exchange] read and write memory information [with] in said memory integrated circuit

LAW OFFICES OF
 SKJERVEN, MORRILL,
 MacPHERSON, FRANKLIN
 & FRIEL

25 METRO DRIVE
 SUITE 700
 SAN JOSE, CA 95110
 (408) 283-1222
 FAX (408) 283-1233

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